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| Class | Subclass | |
| ISSUE CLASSIFICATION | | |

PATENT NUMBER

U.S. UTILITY Patent Application

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REV.
PTO-1305

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PATENT DATE

SCANNED 35 Q.A. 1

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| APPLICATION NO. 09/731726 | CONT/PRIOR F | CLASS 257 148 | SUBCLASS 400 | ART UNIT 2811 1742 | EXAMINER Ip |
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CANTS Kozo Shimizu
Masayuki Ochiai
Yasuo Yamagishi

BEST AVAILABLE COPY

Solder alloy, a circuit substrate, a semiconductor device and a method of manufacturing the same

PTO-2040
12/89

ISSUING CLASSIFICATION

| ORIGINAL | | | | CROSS REFERENCE(S) | | | | | | | | |
|------------------------------|--|----------|--|--------------------|-----------------------------------|--|--|--|--|--|--|--|
| CLASS | | SUBCLASS | | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) | | | | | | | |
| INTERNATIONAL CLASSIFICATION | | | | | | | | | | | | |
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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS Sheets Draw. Figs. Draw. Print Fig. | | CLAIMS ALLOWED Total Claims Print Claim for O.G. | |
| | <input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | | NOTICE OF ALLOWANCE MAILED | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ | _____ (Assistant Examiner) (Date) | | ISSUE FEE | |
| | _____ (Primary Examiner) (Date) | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | ISSUE BATCH NUMBER | |

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